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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(54) Title: VIRTUAL EPROM SIMULATOR APPARATUS

(57) Abstract

An apparatus for simulating the internal configuration of industry standard ROM and EPROM–type chips using other types of storage technologies, while still operating transparently with interfaces and mechanisms such as authentication devices adapted to EPROM–type media. The invention includes: an EPROM connector interface, a data presentation program; user access log display program; a user login/registration program; a software/data library; software/data selection program; and software/data loader program. These components work in conjunction to securely retrieve software images resident in mass storage media and to present them to an authentication device as if the images were resident in EPROM type media. The invention is particularly adapted to use in the gaming industry where regulation and fraud detection are performed using EPROM authentication techniques.